Seven Segment Board

User Manual

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Chapter 1. Seven Segment Board

1. Overview

Seven Segment Board has 8 seven segment LED displays. The board can be used as a display device for displaying decimal numerals as an alternative to more complex dot-matrix displays.

This multiplexed display board has a built-in decoder that needs only 3 control lines to select the required digit and 8 data lines shared to all seven segments. All segments including decimal points are available for control, so that customized readout is made possible. The TTL compatible inputs allows this board work with most micro-controllers and control systems.

2. Features

- Standard 0.56" 7-Segment LED Display
- 8 Digits for counter/timer applications
- · Supports decimal point
- Powered from motherboard via FRC header
- TTL compatible inputs
- Standard 0.1" FRC header for connection to control logic/MCU
- · Ready to go with Zilogic motherboards.

3. Applications

- Digital clocks and calendars.
- · Electronic meters
- · Weighing machines
- Currency displays
- · and other numerical digital read-outs.

4. Locating components

Below diagram explains the location of components on the board

Figure 1.1. Front View

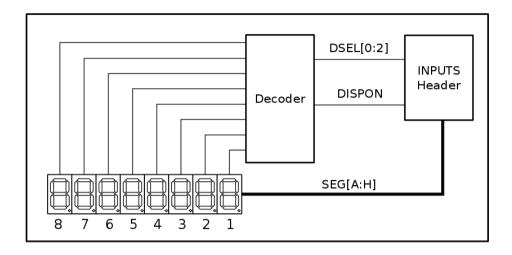


7-Segment Digits

5. Block Diagram

The devices available on the board, is shown in the following block diagram. Each device is described in detail in the following sections.

Figure 1.2. Block Diagram



6. Power Supply

The Seven Segment Board is powered from the motherboard using a FRC-14 connector. The 1st and 14th pin is connected to VCC and GND respectively. Detailed power supply specifications are available in section Specifications.

7. Control Inputs

The Seven Segment Board can be interfaced to the motherboard using the INPUTS header. The SEG signals in the header are used to drive the value to be displayed on a 7-segment. The DSEL signals are used to select the digit position on which the value has to be displayed. The DISPON# signal is used to entire display ON or OFF.

8. Connectors and Headers

8.1. INPUTS Header

The control inputs for the Seven Segment Board is provided through the INPUTS FRC header. The connector details are given below.

Table 1.1. INPUTS Header

Pin#	Display	Signal Type
1	VCC	Supply from motherboard
2	SEG A	TTL In
3	SEG B	TTL In
4	SEG C	TTL In
5	SEG D	TTL In
6	SEG E	TTL In
7	SEG F	TTL In
8	SEG G	TTL In
9	SEG H	TTL In
10	DSEL 0	TTL In

Pin#	Display	Signal Type
11	DSEL 1	TTL In
12	DSEL 2	TTL In
13	DISPON#	TTL In
14	GND	Ground

9. Specifications

Parameter	Value	Condition
VCC		
Voltage	5V	
Max. Current	100mA	
Digital Inputs		
Input Low Voltage	0.0 - 0.8V	
Input High Voltage	2.0 - 5.0V	

Chapter 2. Board Usage

1. Selecting a Digit

A digit of the Seven Segment Board can be selected by using three control lines DSEL0, DSEL1 and DSEL2. The following table shows the DSELx pin states, the corresponding digit selected.

Table 2.1. Digit Selection

DSEL0	DSEL1	DSEL2	DIGIT Selected	
0	0	0	DIGI1	
0	0	1	DIGI2	
0	1	0	DIGI3	
0	1	1	DIGI4	
1	0	0	DIGI5	
1	0	1	DIGI6	
1	1	0	DIGI7	
1	1	1	DIGI8	

2. Displaying a Single Digit

The algorithm for displaying a 7-segment digit is given below.

- 1. Drive the DISPON# high to turn off the display.
- 2. Turn on/off segments by driving the segment signals SEG[A:H]. The segment signals to be driven for each decimal digit is given in the following table.
- 3. Select the 7-segment display by driving DSEL[0:2]
- 4. Drive the DISPON# high to turn on the display.

Figure 2.1. Segment Naming

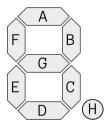


Table 2.2. Character to SEG[A:H] Map

Character	SEG[A:H] in Binary	SEG[A:H] in Hex
0	0b1111_1100	0×FC
1	0b0110_0000	0×60
2	0b1101_1010	0×DA
3	0b1111_0010	0xF2
4	0b1010_1010	0×AA
5	0b1011_1010	0×BA

Character	SEG[A:H] in Binary	SEG[A:H] in Hex
6	0b0111_1101	0×7D
6	0b1011_1110	0×BE
7	0b1110_0000	0×E0
8	0b1111_1110	0×FE
9	0b0110_0111	0×67
9	0b1110_0110	0×E6

The MSB of segment signals (7th bit) is used to switch on the dot separator.

3. Multiplexed 7-Segment Display

Since the signals that drive the segments are shared by the 8 displays, the segments of only one display can be driven at a time. Each display is turned on successively for a small period of time (1.5ms, to avoid flicker), and by persistence of vision all of them appear to be on simultaneously.

Persistence of vision is the phenomenon of the eye by which even nanoseconds of exposure to an image result in milliseconds of sight. — Wikipedia.org

The algorithm for displaying multiple 7-segment digits simultaneously is given below.

- a. Turn off the display.
- b. Select the next 7-segment digit.
- c. Drive the segment data pins corresponding to the value to be displayed.
- d. Turn on the display.
- e. Generate a 1.5ms delay using a timer.
- f. Goto step a.

Appendix A. Legal Information

1. Copying

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2.1. LIMITED HARDWARE WARRANTY

Zilogic Systems warrants that the hardware components of its Hardware Product shall be free from material defects in design, materials, and workmanship and will function, under normal use and circumstances, in accordance with the documentation provided, for a period of one (1) year from the date of purchase of the Hardware Product.

Your sole and exclusive remedy, and Zilogic Systems' sole and exclusive liability for defective hardware components, shall be that Zilogic Systems, subject to the terms and conditions of this Section, and solely upon confirmation of a defect or failure of a hardware component to perform as warranted, shall at its sole option, either repair or replace the nonconforming hardware component. All replacement parts furnished to you under this warranty shall be refurbished and equivalent to new, and shall be warranted as new for the remainder of the original warranty period. All defective parts, which have been replaced, shall become the property of Zilogic Systems. All defective parts that have been repaired shall remain your property.

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If a Hardware Product or one of its component parts does not function as warranted during the warranty period, and such nonconformance can be verified by Zilogic Systems, Zilogic Systems, at

its election, will provide either return and replacement service or replacement with a refurbished part/unit for the Hardware Product under the type of warranty service Zilogic Systems designates for that Hardware Product. A defective Hardware Product or one of its component parts may only be returned to Zilogic Systems upon Zilogic Systems' prior written approval. Any such approval shall reference an RMA number issued by an authorized Zilogic Systems service representative. If you do not register the Hardware Product with Zilogic Systems, you may be required to present proof of purchase as evidence of your entitlement to warranty service. The Hardware Product's serial number will be required for all RMA cases.

Transportation costs, if any, incurred in connection with the return of a defective item to Zilogic Systems shall be borne by You. Any transportation costs incurred in connection with the redelivery of a repaired or replacement item to You by Zilogic Systems shall be borne by Zilogic Systems; provided, however, that if Zilogic Systems determines, in its sole discretion, that the allegedly defective item is not covered by the terms and conditions of the warranty or that a warranty claim is made after the warranty period, the cost of the repair by Zilogic Systems, including all shipping expenses, shall be reimbursed by You.

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Zilogic Systems will attempt to diagnose and resolve your problem over the phone or e-mail. Upon determination of the hardware issue is related to a malfunction of one of the Hardware Product components, an RMA process will be initiated by Zilogic Systems.

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- To ship back the faulty Hardware Product (or replaceable unit) suitably packaged, quoting the RMA number, to the Zilogic Systems designated location.
- You shall ship the faulty Hardware Product once Zilogic Systems approves the RMA and provide the courier name and tracking number.
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